

NON-VOLATILE MEMORY ARRAY HAVING VERTICAL TRANSISTORS
AND MANUFACTURING METHOD THEREOF
Application No. [New] – Attorney Doc. No. LEE.002
Inventor: Shone FUJA

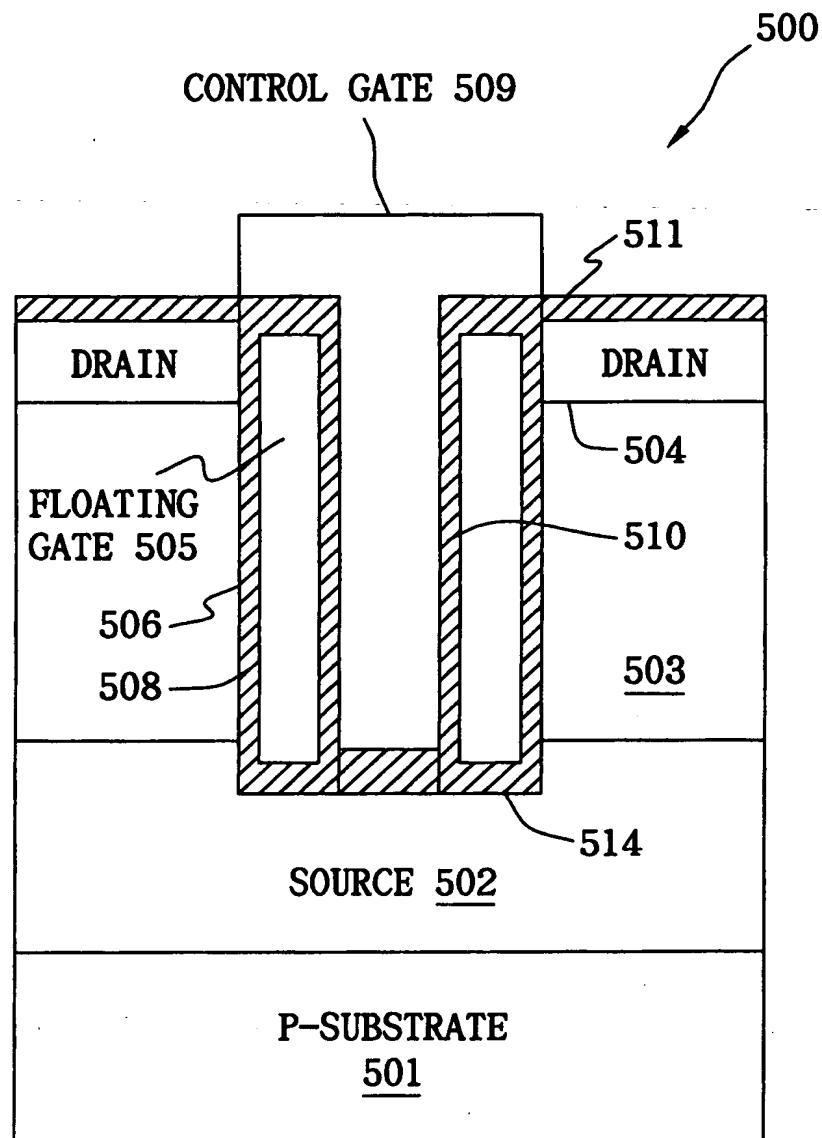


FIG. 1(a) (Background Art)

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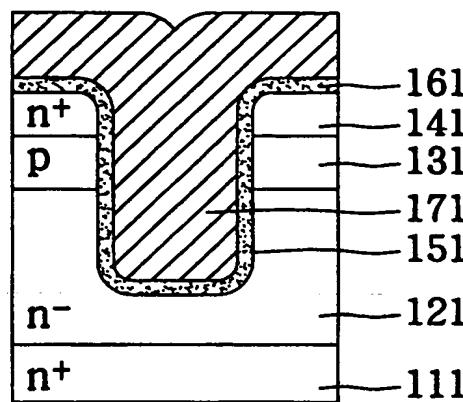


FIG. 1(b) (Background Art)

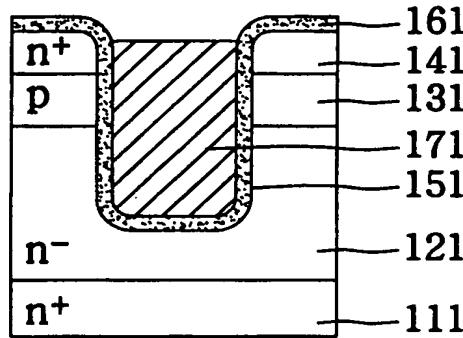


FIG. 1(c) (Background Art)

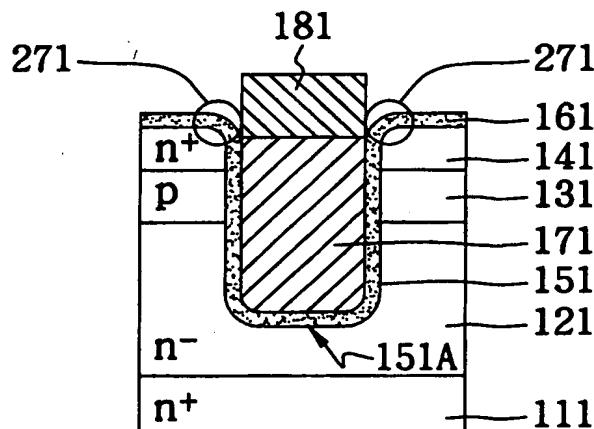


FIG. 1(d) (Background Art)

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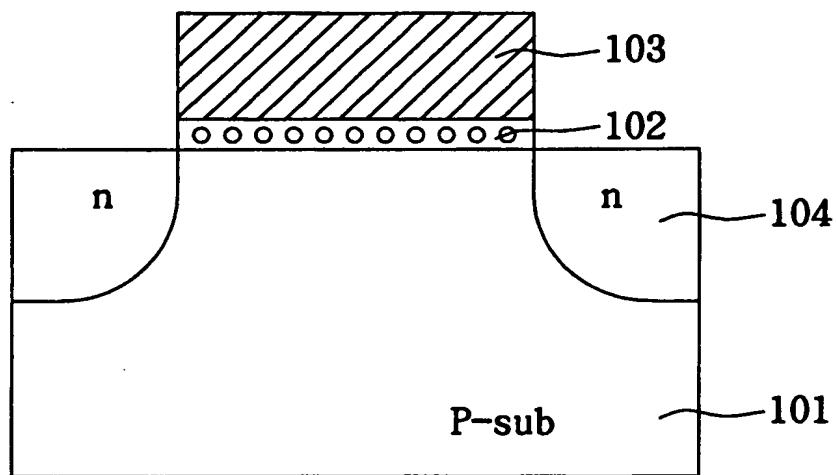
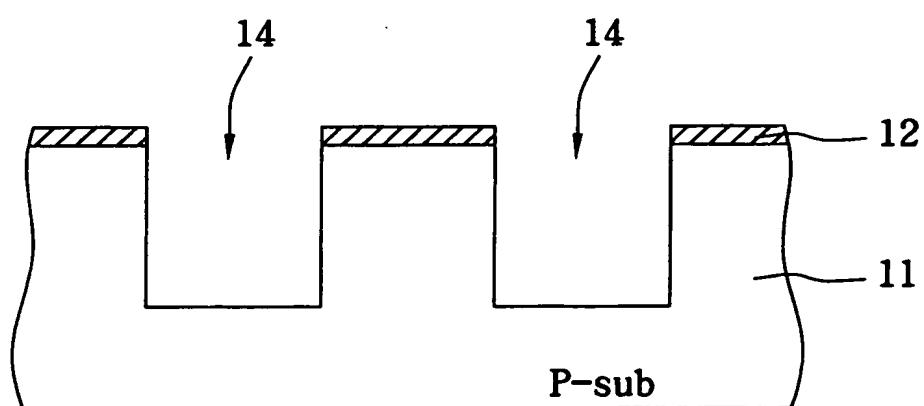
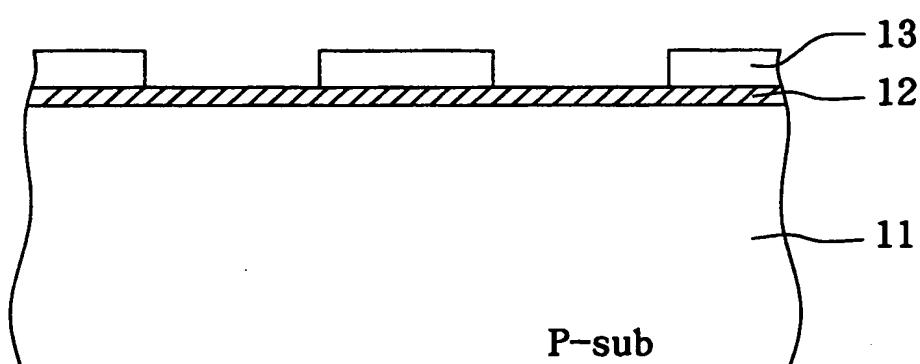
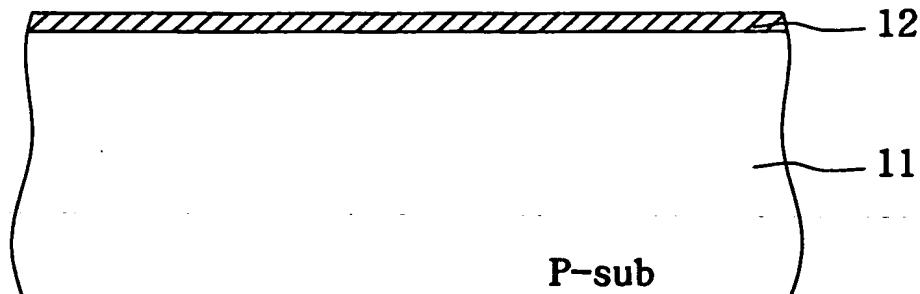


FIG. 1(e) (Background Art)

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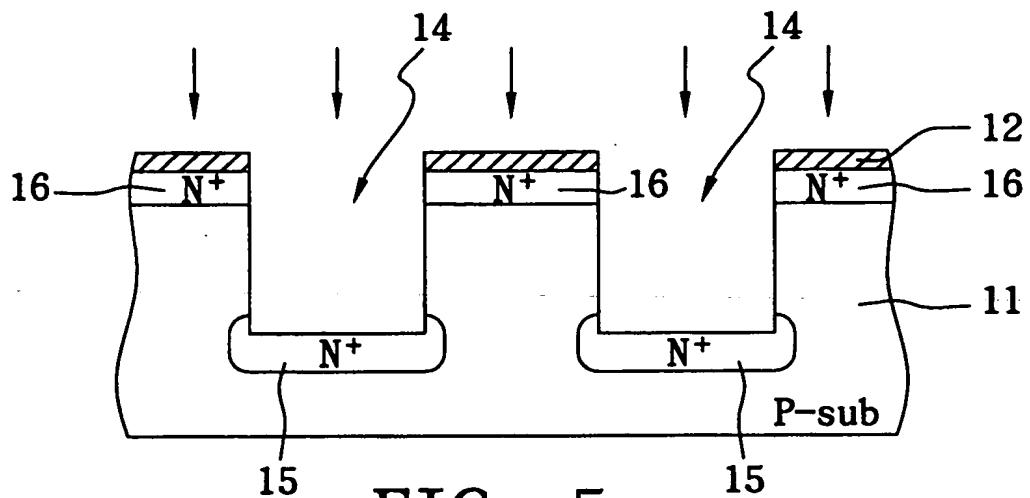


FIG. 5

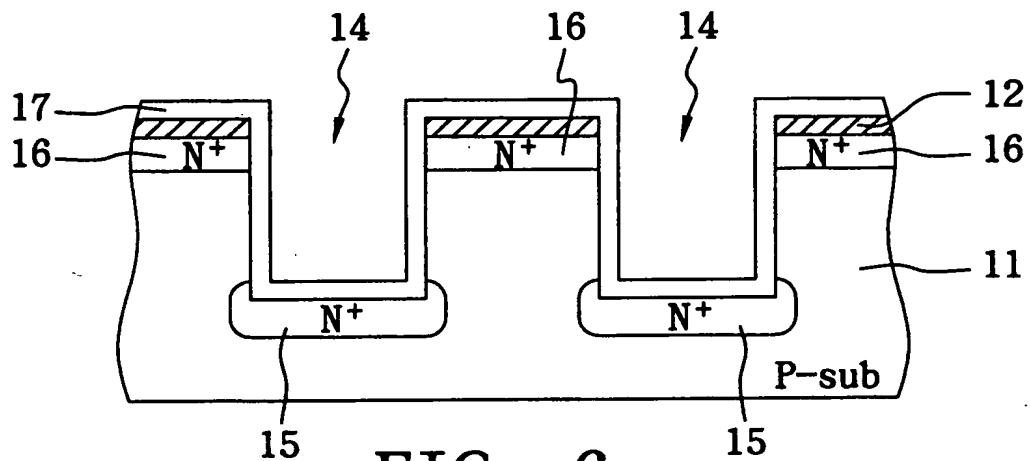


FIG. 6

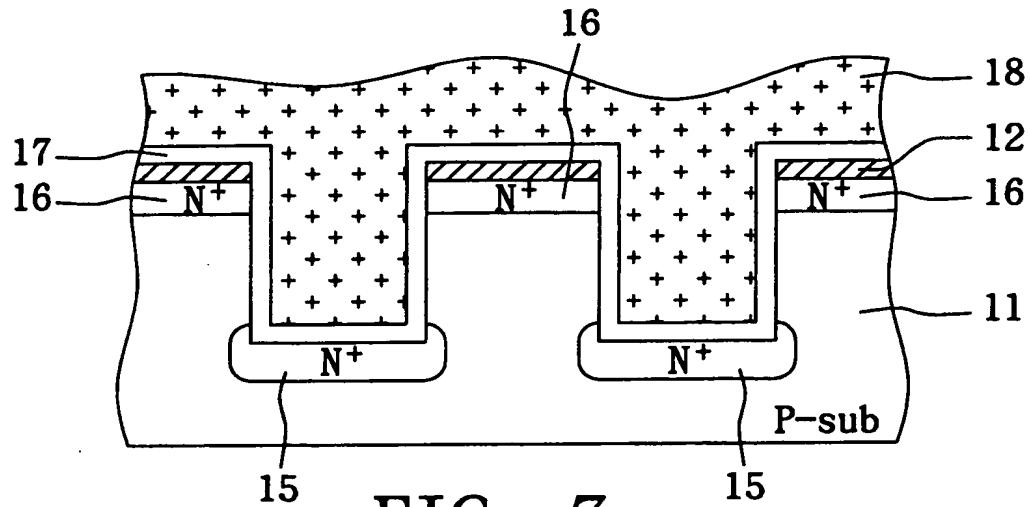


FIG 7

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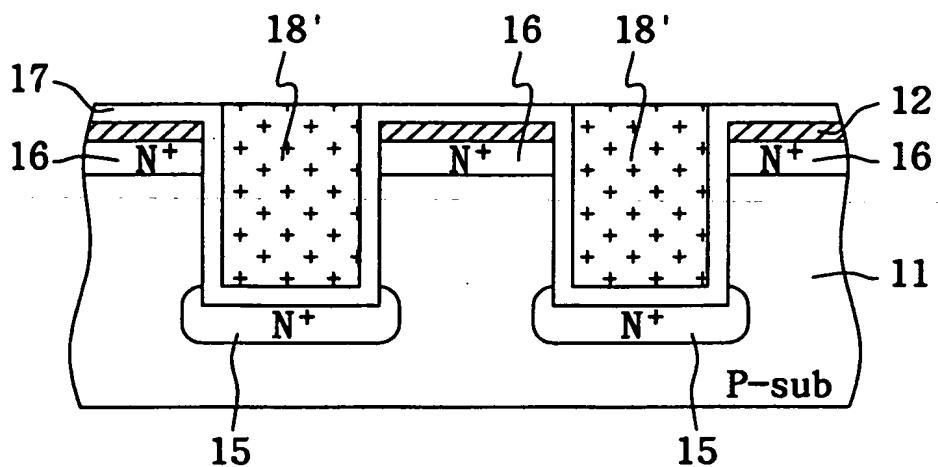


FIG. 8

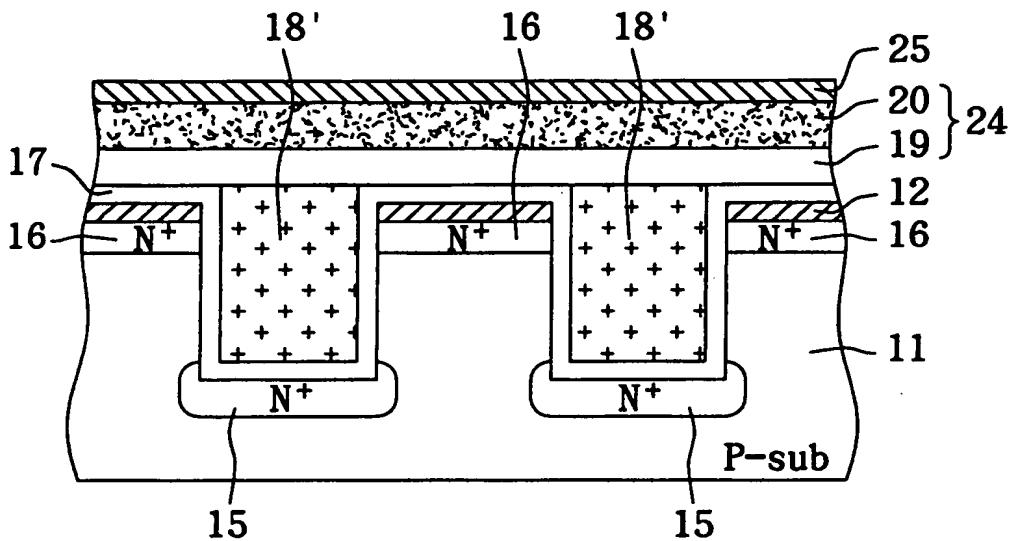


FIG. 9

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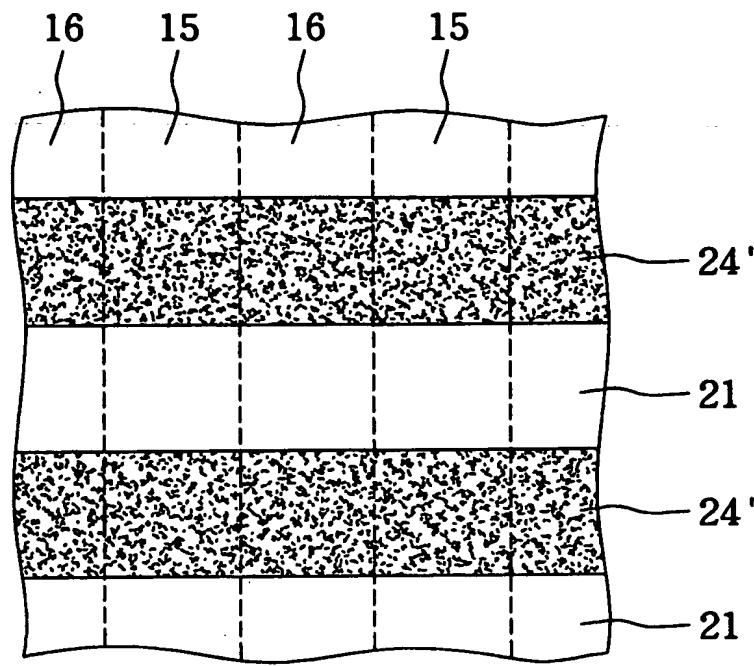


FIG. 10

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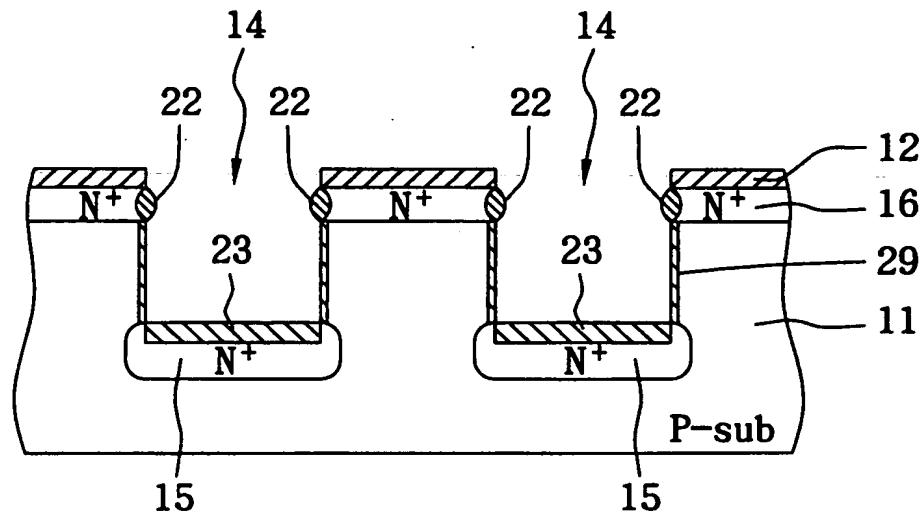


FIG. 11

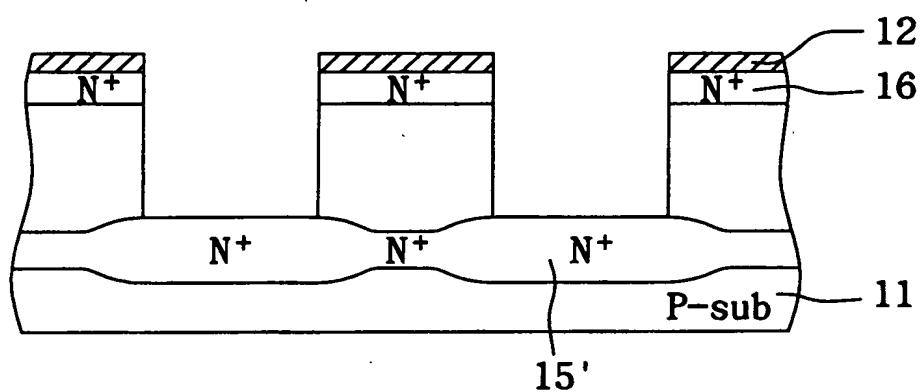


FIG. 12

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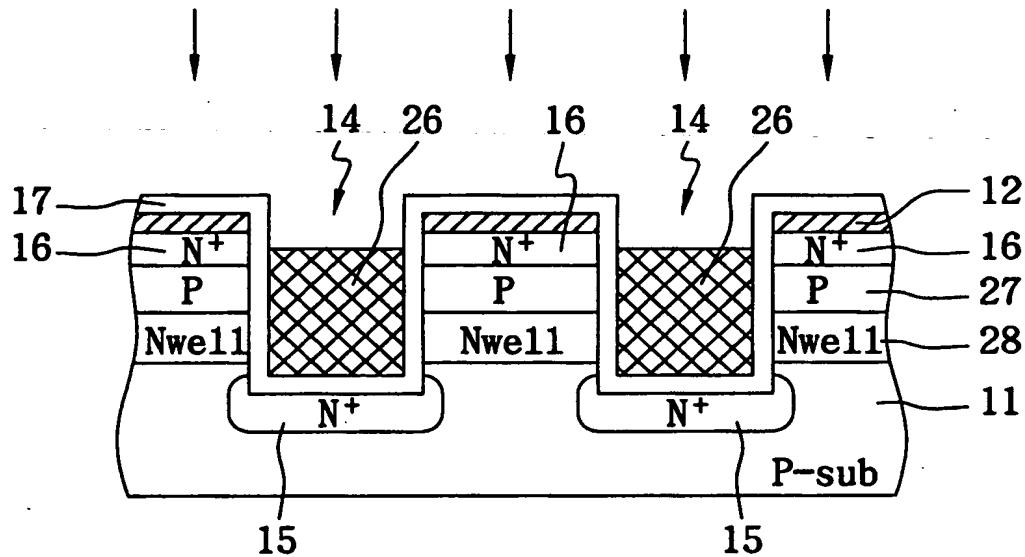


FIG. 13

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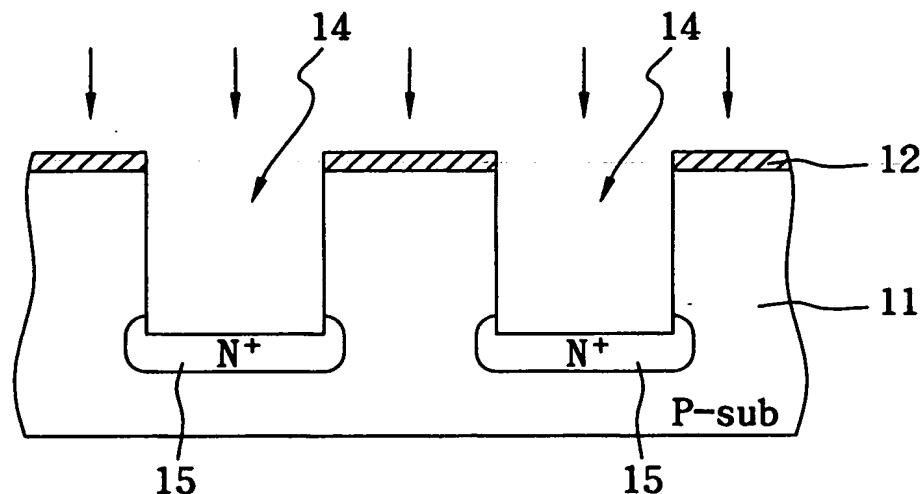


FIG. 14

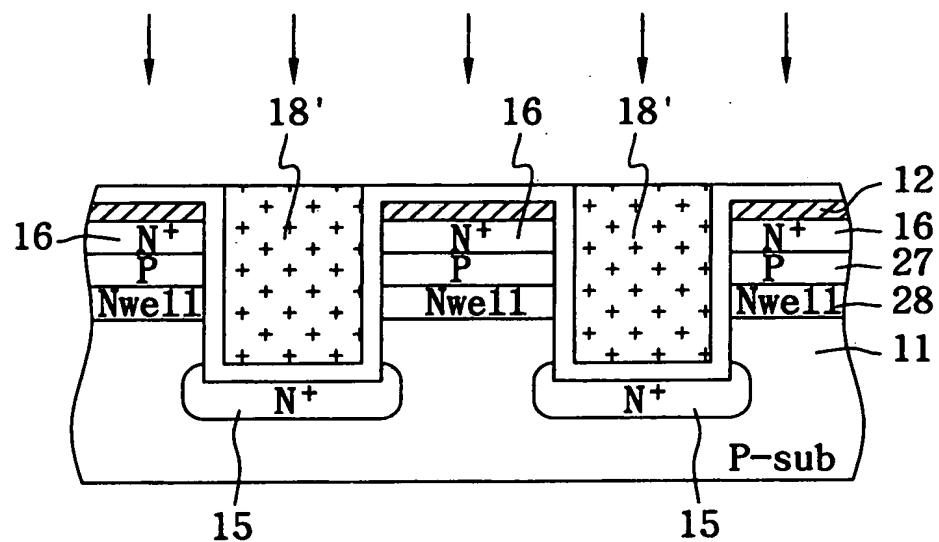


FIG 15

**NON-VOLATILE MEMORY ARRAY HAVING VERTICAL TRANSISTORS
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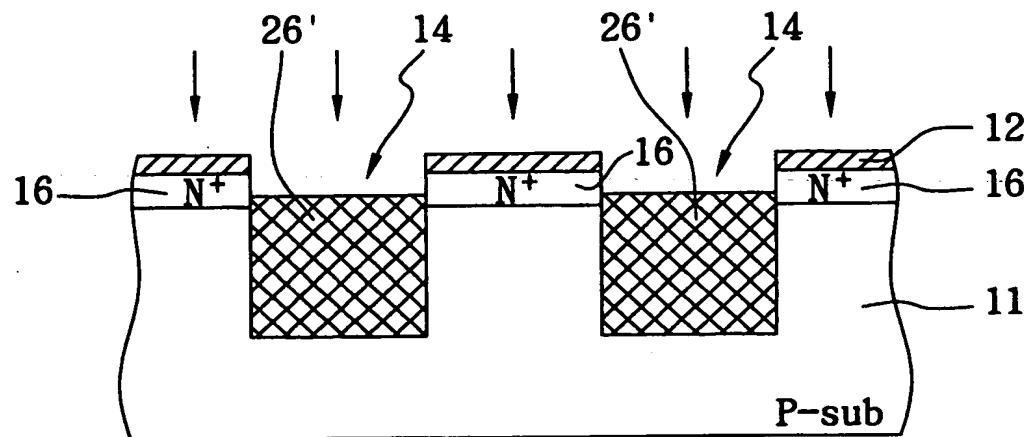


FIG. 16

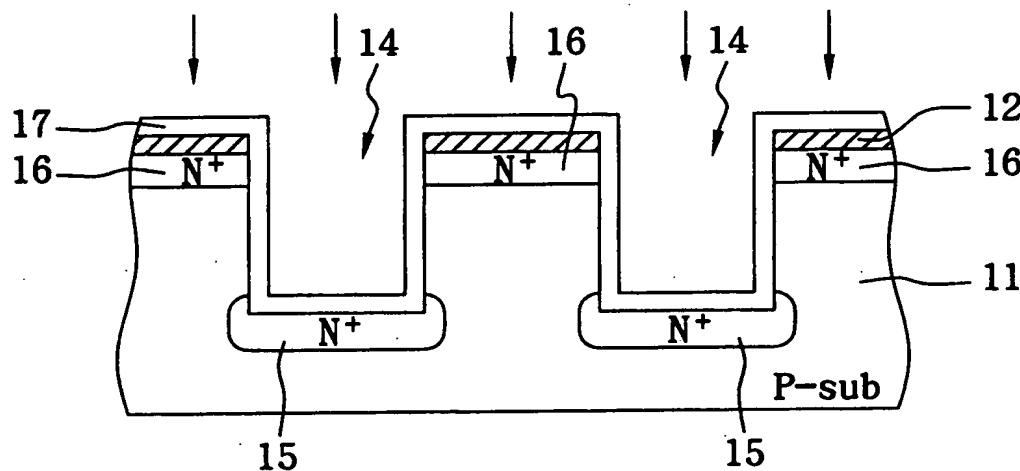


FIG. 17

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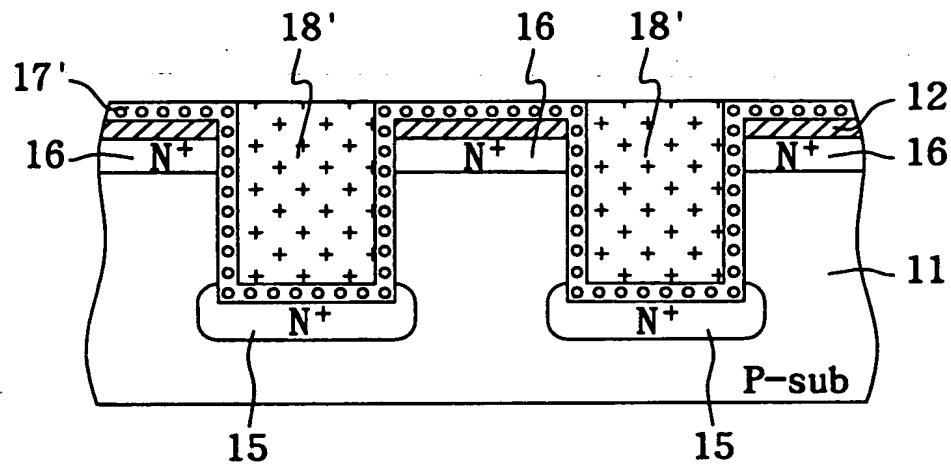


FIG. 18